

**INFORMATION DISCLOSURE CITATION**  
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Docket Number (Optional)

POU920020090US1

Application Number

Applicant(s)

Ira L. Chavis Express Mail Label# ER 363647616 US

Filing Date

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Group Art Unit

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4715818	12/29/87	Shapiro et al	434	118	12/4/84
		4876657	10/24/89	Saito et al	364	521	8/5/87
		5107443	9/7/88	Smith et al	395	158	9/7/88
		5339389	8/16/94	Bates et al	395	153	12/31/91
		5539658	7/23/96	McCullough et al	364	514	5/16/95
		5581702	12/3/96	McArdle et al	395	200.04	12/20/93
		5634018	5/27/97	Tanikoshi et al	395	329	9/12/94
		5721852	2/24/98	Porter	395	349	6/24/96
		5859974	1/12/99	McArdle et al	395	200.34	7/8/96
		5892509	4/6/99	Jakobs et al	345	331	4/4/94
		5917480	6/29/99	Tafoya et al	345	302	6/4/96

**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

		Intel 82830M Graphics Controller, Multi-Monitor: Understanding Options For Multiple Displays, 5 pages from <a href="http://support.intel.com/support/graphics/intel830m/tti011.htm">http://support.intel.com/support/graphics/intel830m/tti011.htm</a>

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**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5995096	11/30/99	Kitahara et al	345	330	12/18/97
	5944785	8/31/99	Pommier et al	709	205	3/3/97
	6037943	3/14/2000	Crone et al	345	357	8/10/95
	6091408	7/18/2000	Treibitz et al	345	329	8/13/97
	6178431	1/23/01	Douglas	707	512	10/5/94
	6308199	10/23/01	Katsurabayashi	709	204	8/7/98
	6324553	11/27/01	Cragun et al	707	513	11/26/97

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						YES	NO

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